

What is claimed is:

Draft A1

1. A liquid crystal display (LCD) panel comprising:
 - first and second substrates;
 - an active region on the first substrate, the active region including a plurality of thin film transistors and pixel electrodes;
 - a sealing region along a periphery of the active region and along a corresponding region of the second substrate;
 - sealant in the sealing region, the sealant attaching the first substrate and the second substrate to one another and maintaining a gap therebetween;
 - a light-shielding layer on the second substrate, the light-shielding layer being substantially absent in the sealing region at least where the sealant attaches to the second substrate; and
 - a liquid crystal layer within the gap and on the active region side of the sealant.
2. The LCD panel of claim 1, wherein the sealant is an UV-type hardening sealant.
3. The LCD panel of claim 1, wherein the light-shielding layer is formed of a light-shielding material.
4. The LCD panel of claim 1, wherein the light-shielding layer has a matrix arrangement in the active region.

5. A method of manufacturing an LCD panel comprising the steps of:
forming a plurality of pixel electrodes in an active region on a first substrate;
applying UV-type hardening sealant on a sealing region positioned along a
periphery of the active region;
forming a light-shielding layer on a second substrate except at least in areas where
the UV-type hardening sealant is to be attached;
attaching the first and second substrates to each other; and
irradiating UV-rays to the sealant.

6. The method of claim 5, wherein the step of forming the light-shielding layer
includes:
forming a light-shielding material on the second substrate; and
etching the light-shielding material using a mask, wherein the mask includes a
first portion corresponding to a matrix arrangement in the active region and a second portion
corresponding to the sealing region.

7. The method of claim 5, further comprising the step of forming a plurality of color
filter patterns on the second substrate after forming the light-shielding layer.

8. A method of manufacturing an LCD panel comprising the steps of:
forming an UV-type hardening sealant in a first sealing region of a first substrate;
dropping liquid crystal on a surface of the first substrate;

forming a light-shielding layer on portions of a second substrate for preventing light from being transmitted through the portions of the second substrate and allowing light to transmit through the other portion of the substrate, wherein the portions do not include a second sealing region of the second substrate where attachment to the sealant is intended; attaching the first and second substrates to each other; and irradiating the sealant with UV-rays.

9. The method of claim 8, further comprising the step of forming a spacer on any one of the first and second substrates.

10. The method of claim 8, wherein the step of forming the light-shielding layer includes:

forming a light-shielding material on the second substrate; and etching the light-shielding material using a mask, wherein the mask includes a first portion corresponding to a matrix arrangement in the active region and a second portion corresponding to the second sealing region, the first and second portions of the mask corresponding to the other portions of the second substrate.

11. The method of claim 8, further comprising the step of forming a plurality of color filter patterns on the second substrate after the step of forming the light-shielding layer.

12. The method of claim 8, further comprising the step of forming thin film transistors and pixel electrodes on the first substrate.

13. A liquid crystal display (LCD) panel comprising:

- a first substrate including a plurality of pixel electrodes;
- a second substrate opposing the first substrate;
- a first sealing area of the first substrate surrounding the pixel electrode;
- a second sealing area of the second substrate corresponding to the first sealing area;

UV-type hardening sealant between the first and second substrates and contacting the first and second sealing areas; and

- a light-shielding layer on the second substrate, the light-shielding layer absent from at least the second sealing area.

14. The LCD panel of claim 13, wherein the light-shielding layer is arranged in a matrix configuration within an active area surrounded by the second sealing area.

15. The LCD panel of claim 13, wherein the first substrate further includes at least one thin film transistor.

16. The LCD panel of claim 13, further comprising one or more spacers on at least one of the first and second substrates.

17. The LCD panel of claim 13, further comprising a color filter layer on the second substrate.

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